



PTO/SB/08A (10-01)  
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Substitute for form 1449A/PTO		<b>Complete if Known</b>	
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> (use as many sheets as necessary)		Application Number	09/815,921
		Filing Date	March 24, 2001
		First Named Inventor	Bindiganavale S. Nataraj
		Art Unit	2661
		Examiner Name	Not Yet Assigned
Sheet 1	of 7	Attorney Docket Number	NLM.P002

U.S. PATENT DOCUMENTS					
Examiner Initials <sup>1</sup>	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code <sup>2</sup> (if known)			
CLD		US- 3,257,646	06-21-1966	Roth	
CLD		US- 3,353,159	11-14-1967	Lee, III	
CLD		US- 3,602,899	08-31-1971	Lindquist et al.	
CLD		US- 3,675,211	07-04-1972	Raviv	
CLD		US- 3,685,020	08-15-1972	Meade	
CLD		US- 3,868,642	02-25-1975	Sachs	
CLD		US- 4,112,502	09-05-1978	Scheuneman	
CLD		US- 4,244,033	01-06-1981	Hattori	
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CLD		US- 4,523,301	06-11-1985	Kadota	
CLD		US- 4,646,271	02-24-1987	Uchiyama	
CLD		US- 4,656,626	04-07-1987	Yudichak et al.	
CLD		US- 4,670,858	06-02-1987	Almy	
CLD		US- 4,747,080	05-24-1988	Yamada	
CLD		US- 4,758,982	07-19-1988	Price	
CLD		US- 4,780,845	10-25-1988	Threewitt	
CLD		US- 4,785,398	11-15-1988	Joyce et al.	
CLD		US- 4,791,606	12-13-1988	Threewitt et al.	
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CLD		US- 4,845,668	07-04-1989	Sano et al.	

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FOREIGN PATENT DOCUMENTS					
Examiner Initials <sup>1</sup>	Cite No. <sup>1</sup>	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Country Code <sup>3</sup> - Number <sup>4</sup> - Kind Code <sup>5</sup> (if known)			
CLD		EP 0 267 612 A2	05-18-1988	Akita	
CLD		EP 0 381 249 A2	08-08-1990	Akashi	
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CLD		EP 08167295 (Japan Abst)	06-25-1996	Hiroshi	
CLD		EP 08273376 (Japan Abst)	10-18-1998	Takeshi	
CLD		WO 98/12651	03-26-1998	Kemoko	
CLD		WO 99/31581	06-24-1999	Therkildsen	

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Substitute for form 1449B/PTO		<b>Complete if Known</b>	
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		Group Art Unit	2661
		Examiner Name	Not Yet Assigned
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Sheet	5	of	7

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS			
Examiner Initials <sup>1</sup>	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T <sup>2</sup>
CLD		Advanced Micro Devices, "Final Am99C10A 256 x 48 Content Addressable Memory" Publication No. 08125, Rev. G, December 1992, pp. 1-21.	
CLD		MASAO AKATA, "A Scheduling Content-Addressable Memory for ATM Space-Division Switch Control", IEEE International Solid-State Circuits Conference, Feb. 1991, New York.	
CLD		SOO-IK CHAE et al., "Content-Addressable Memory for VLSI Pattern Inspection", IEEE Journal of Solid State Circuits, Vol. 23, No. 1, February 1988, pp. 74-78.	
CLD		Cisco Systems, Inc., "Policy-Based Routing", 1996, pp. 1-7.	
CLD		GEC Plessey Semiconductors, "P2800 Multi-Port Content Addressable Memory", Functional Product Specification GPS-FPS-2800-12, January 1986.	
CLD		GEC Plessey Semiconductors, "P2800 2K x 64 Bit Multi-Port Content Addressable Memory", Preliminary Information, February 1997, pp. 1-15.	
CLD		KANAD GHOSE, "Response Pipelined CAM Chips: The First Generation and Beyond.", 7th International Conference on VLSI Design, January 1994, pp. 365-368.	
CLD		PANKAJ GUPTA et al., "Routing Lookups in Hardware at Memory Access Speeds", Proc. Infocom, April 1988, 8 pgs.	
CLD		PANKAJ GUPTA and NICK MCKEOWN, "Packet Classification on Multiple Fields", SIGCOMM (September 2, 1999), 14 pages.	
CLD		SIMON R. JONES et al., "A 9-kbit Associative Memory for High Speed Parallel Processing Applications", IEEE Journal of Solid-State Circuits, Vol. 23, No. 2, April 1988, pp. 543-548.	
CLD		HIROSHI KADOTA et al., "An 8-kbit Content-Addressable and Reentrant Memory", IEEE Journal of Solid-State Circuits, Vol. SC-20, No. 5, October 1985, pp. 951-956.	

Examiner Signature		Date Considered	8/19/05
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<b>Sheet</b>	6	<b>of</b>	7

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CLD		KLSI KE5B064A1 Address Processor Ver. 2.0.2 Preliminary (believed to be published late 1995 or early 1996).	
CLD		KLSI KE5B064H Series Address Processor Ver. 1.0.1 (believed to be published in late 1995 or early 1996).	
CLD		MASAYOSHI KOBAYASHI et al., "A 50Mbps Longest Prefix Match Search Engine LSI for Multigigabit IP Forwarding", Technical Report of IEICE IN08-119, November 1998, pp. 7-12.	
CLD		T.V. LAKSHMAN and D. STILIADIS, "High-Speed Policy-Based Packet Forwarding Using Efficient Multi-Dimensional Range Matching", SIGCOMM, 1988, pp. 203-214.	
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CLD		ANTHONY MCAULEY et al., "A Self-Testing Reconfigurable CAM", IEEE Journal of Solid-State Circuits, Vol. 26, No. 3, March 1991, pp. 257-261.	
CLD		Motorola Semiconductor Technical Data, "Advanced Information 16K X 64 CAM", MCM69C432, January 1996, 4 pages.	
CLD		MUSIC Semiconductors, "MU9C1480 LANCAM" Advance Information, March 22, 1990, pp. 1-11.	
CLD		MUSIC Semiconductors, "MU9C1480 LANCAM Handbook", November 1994.	
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CLD		MUSIC Semiconductors, "MUAA CAM Family" Advanced Information, February 24, 1998, Rev. 0, pp. 1-16.	

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